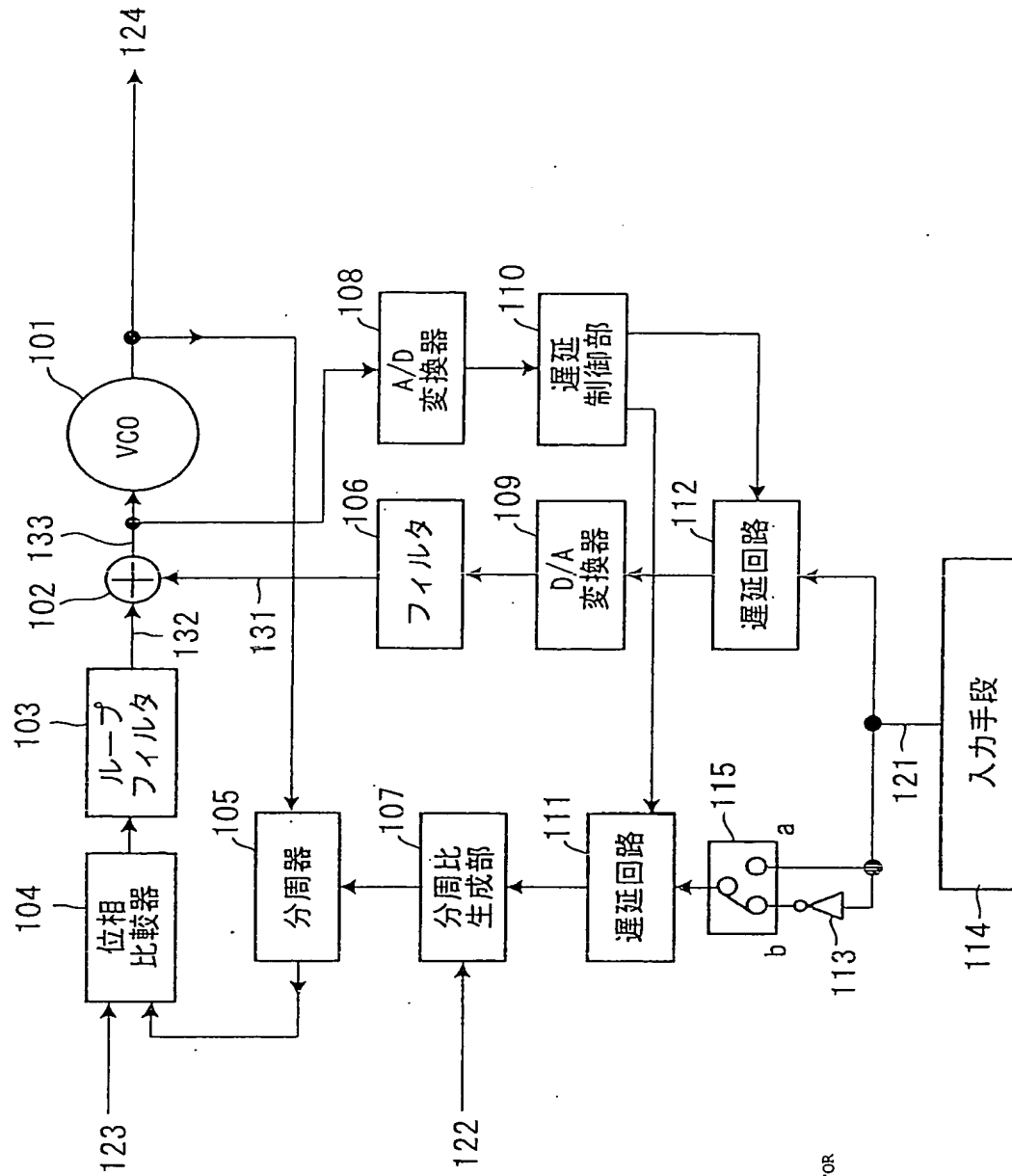


Fig. 1



103 .. LOOP FILTER

104 .. PHASE COMPARATOR

105 .. FREQUENCY DIVIDER

106 .. FILTER

107 .. FREQUENCY DIVIDING RATIO GENERATOR

108 .. A/D CONVERTER

109 .. D/A CONVERTER

110 .. DELAY CONTROLLER

111 .. DELAY CIRCUIT

112 .. DELAY CIRCUIT

114 .. INPUT MEANS

Fig. 2

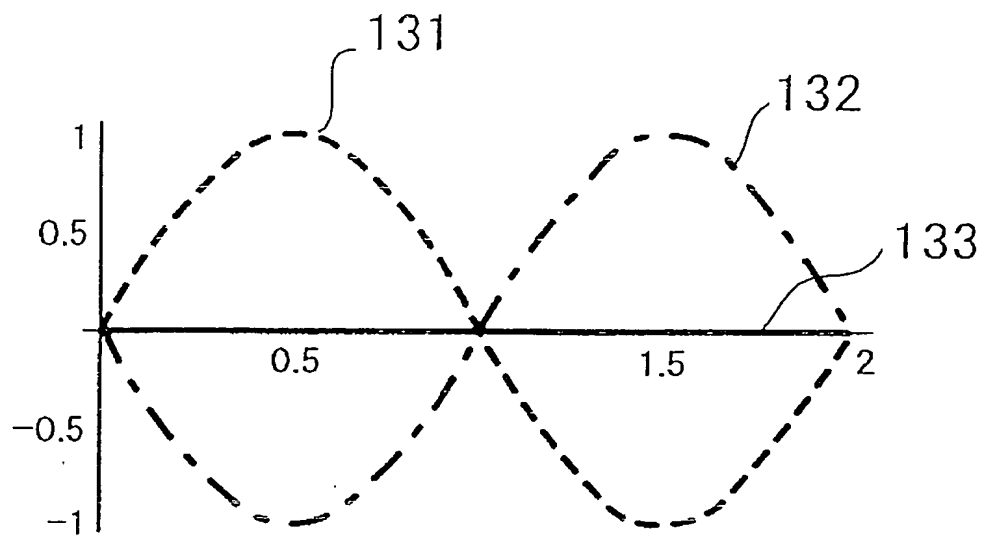


Fig. 3

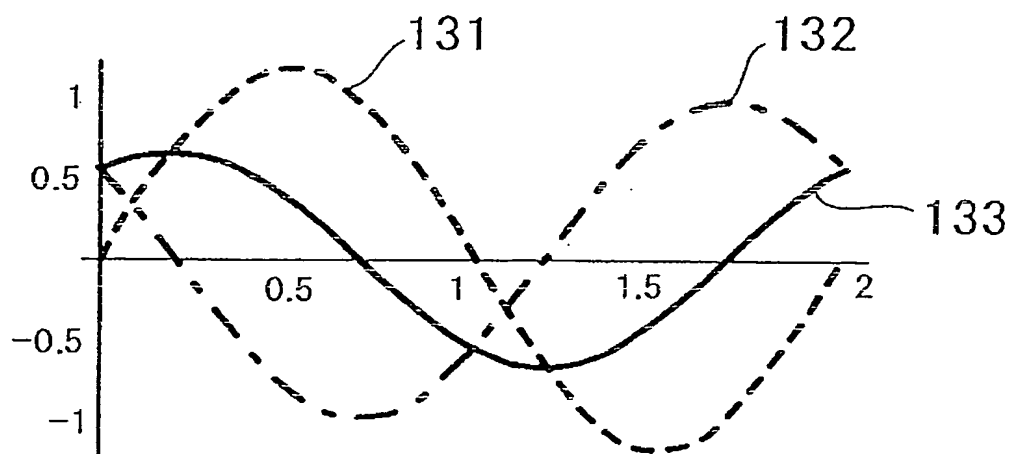


Fig. 4

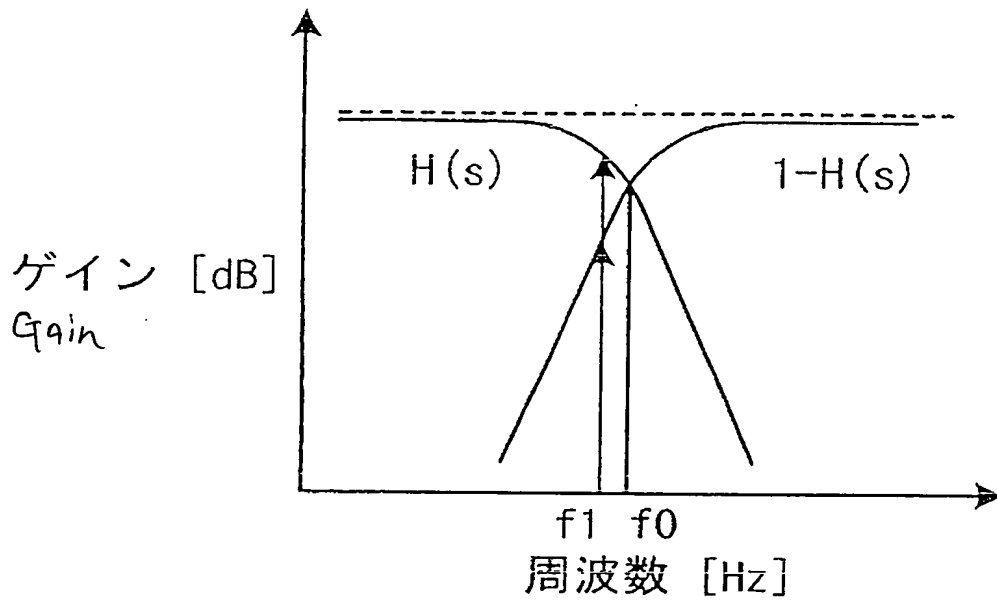


Fig. 5

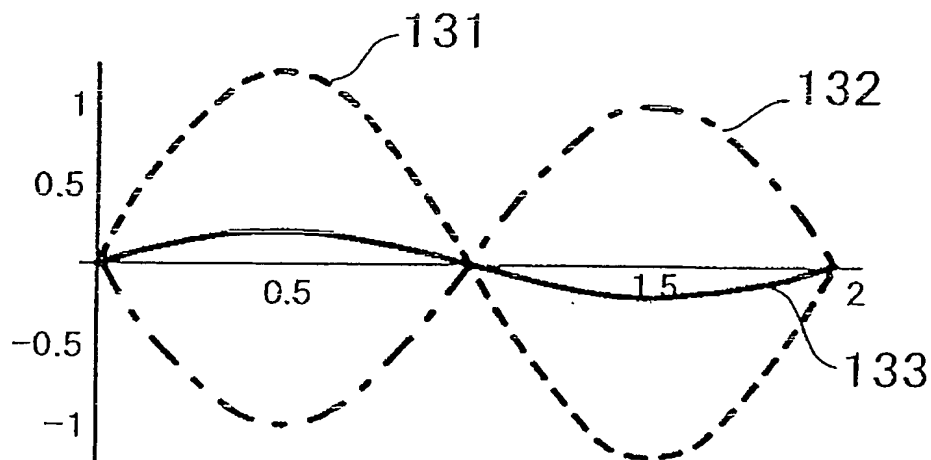


Fig. 6

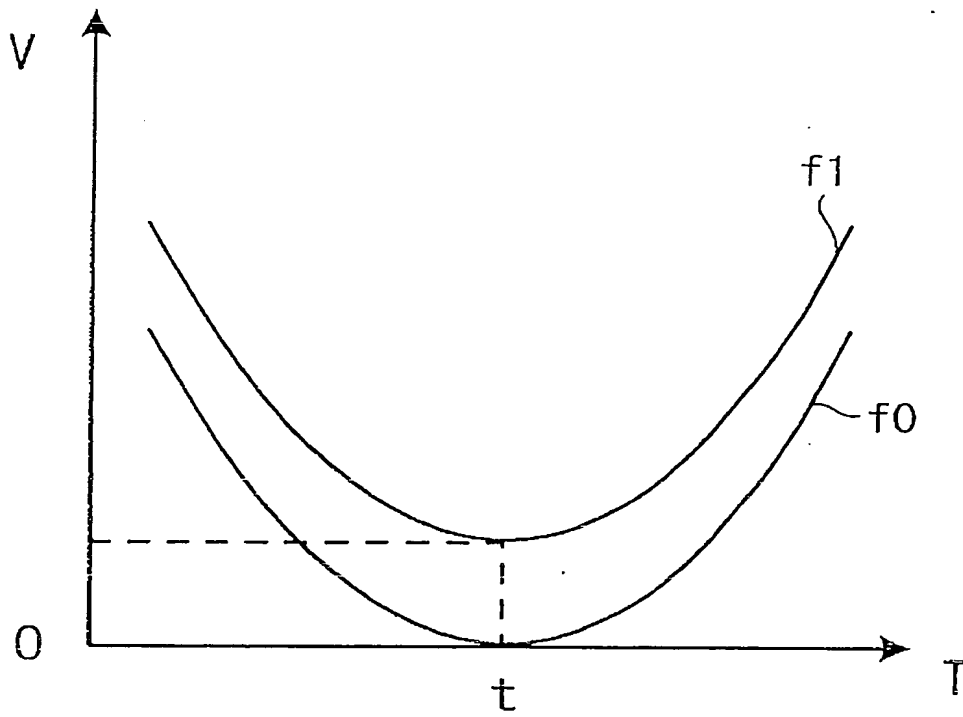
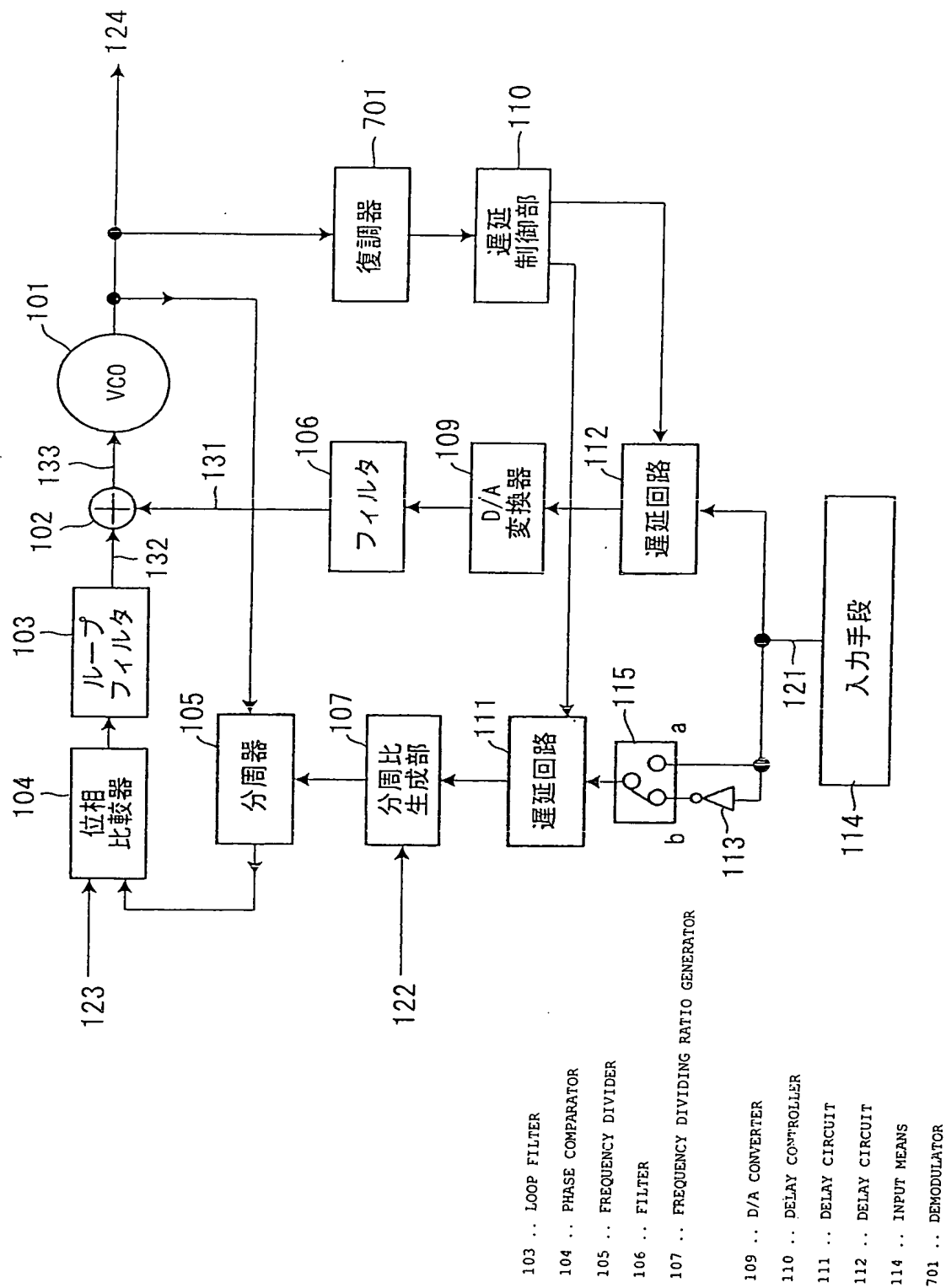
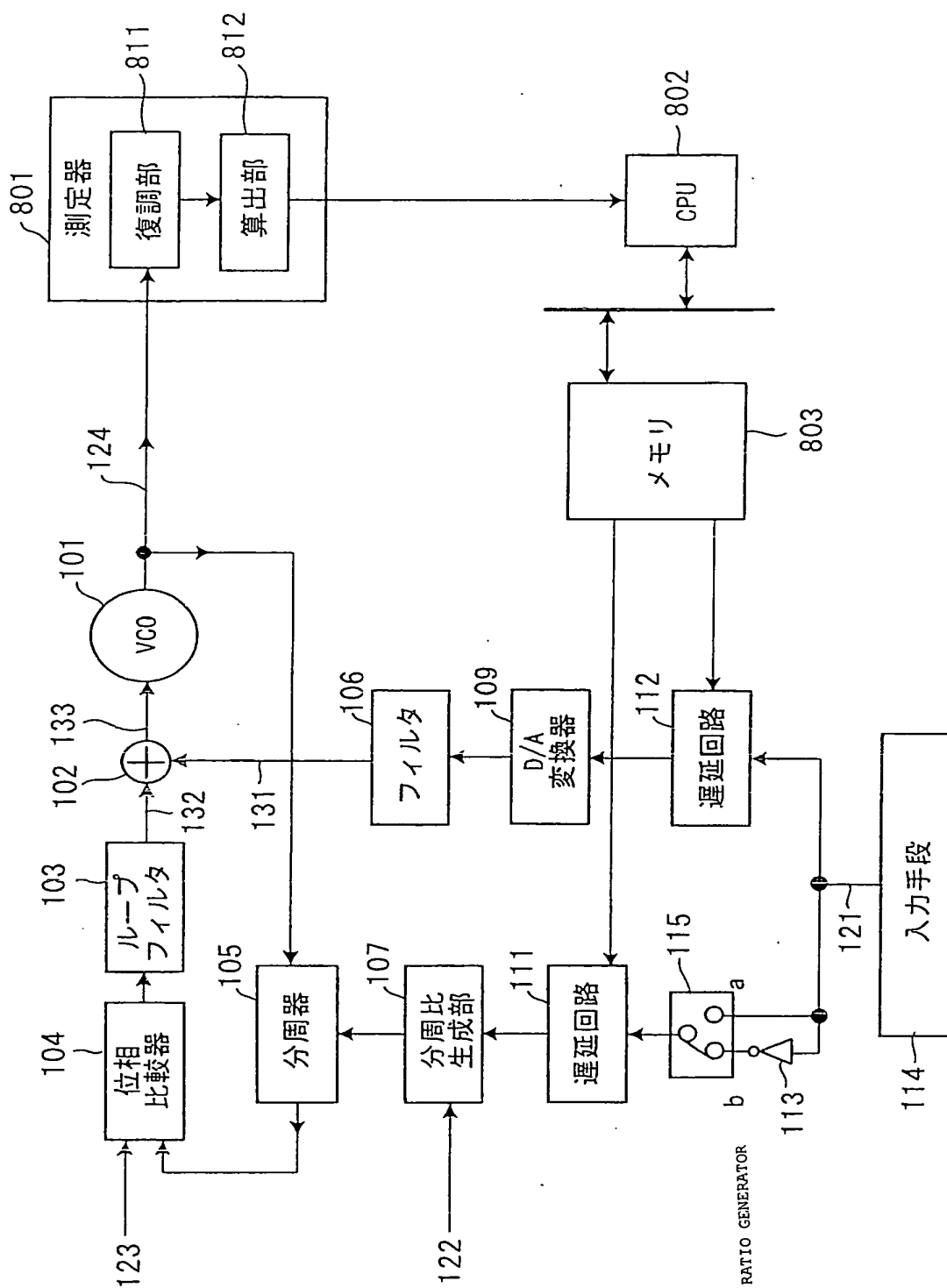


Fig. 7





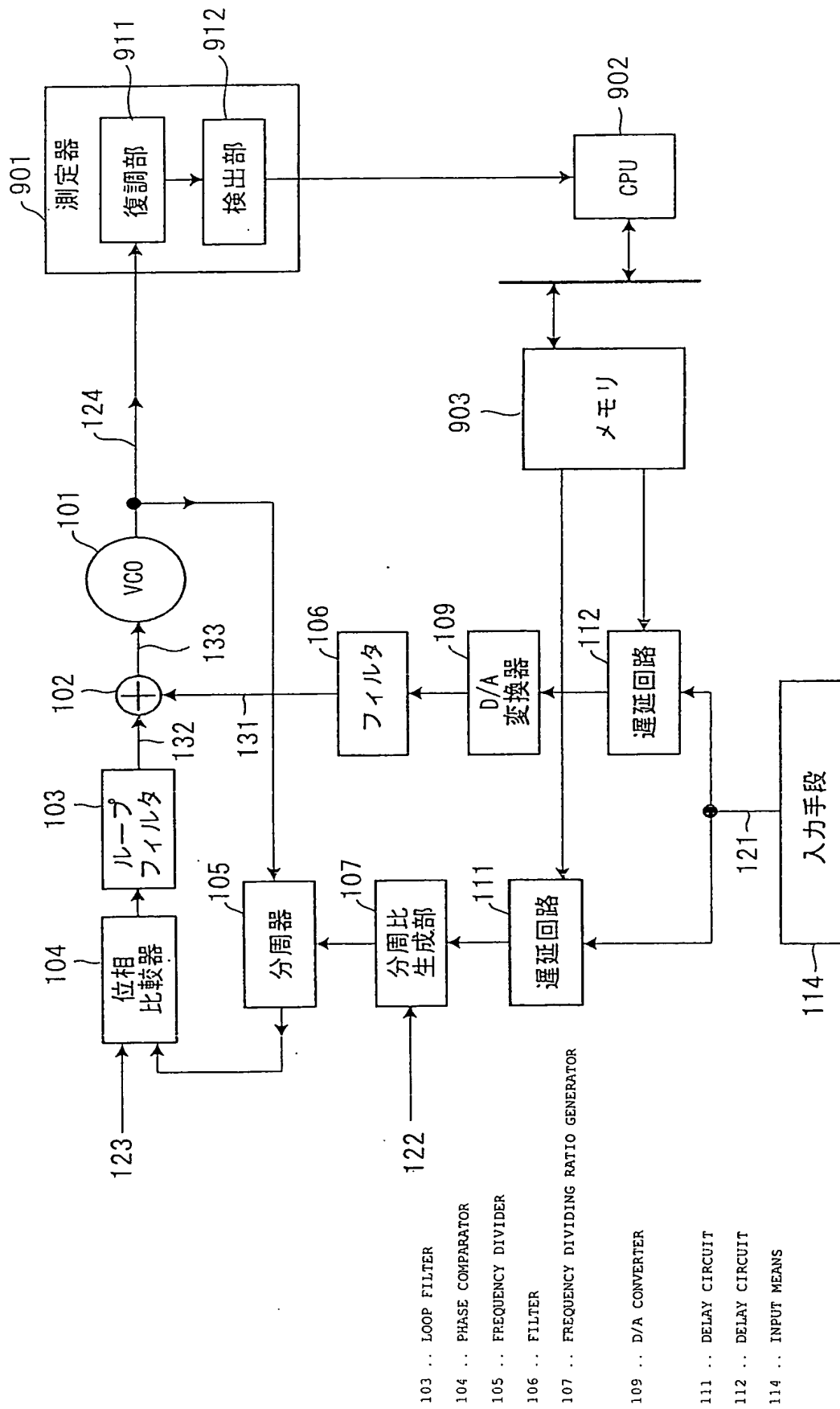
6/8

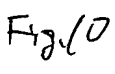
- ```

103 .. LOOP FILTER
104 .. PHASE COMPARATOR
105 .. FREQUENCY DIVIDER
106 .. FILTER
107 .. FREQUENCY DIVIDING
109 .. D/A CONVERTER
111 .. DELAY CIRCUIT
112 .. DELAY CIRCUIT
114 .. INPUT MEANS
801 .. MEASURING UNIT
803 .. MEMORY
811 .. DEMODULATOR
812 .. CALCULATOR

```

图 9





- |       |                        |
|-------|------------------------|
| A ..  | REFERENCE SIGNAL       |
| B ..  | RF MODULATION SIGNAL   |
| C ..  | CHANNEL SELECTION      |
| D ..  | GAIN CONTROL SIGNAL    |
| E ..  | VCO CONTROL VOLTAGE    |
| F ..  | MODULATION DATA        |
| 2 ..  | FREQUENCY DIVIDER      |
| 3 ..  | PHASE COMPARATOR       |
| 4 ..  | LOOP FILTER            |
| 6 ..  | CONTROLLER             |
| 7 ..  | MODULATION SENSITIVITY |
| 9 ..  | DELTA SIGMA MODULATOR  |
| 10 .. | D/A CONVERTER          |
| 11 .. | A/D CONVERTER          |